

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): A. Satoh et al.
Docket No.: JP920020242US1
Serial No.: To Be Assigned
Filing Date: January 21, 2004
Group: To Be Assigned
Examiner: To Be Assigned

"Express Mail" Label No. EV327556285US
I hereby certify that this paper or fee is being deposited with the
United States Postal Service "Express Mail Post Office to
Addressee" service under 37 CFR 1.10 on the date indicated
below and is addressed to: Commissioner for Patents, P.O. Box
1450, Alexandria, VA 22313-1450.

Date of Deposit: January 21, 2004

Signature: 

Title: Multiplier and Cipher Circuit

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

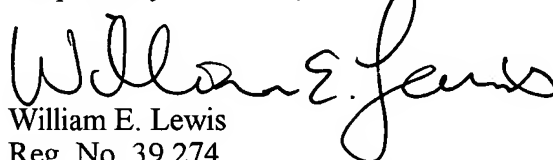
Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicants' attorney wishes to bring to the attention of the Patent and Trademark Office the following document listed on the accompanying Form PTO-1449. A copy of the listed document is enclosed.

Other Documents

1. Johann Großschädl, "A Bit-Serial Unified Multiplier Architecture for Finite Fields GF(p) and GF(2^m)," CHES 2001, LNCS 2162, pp. 202-219, 2001.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability, or as a representation that no other material information exists.

Respectfully submitted,



William E. Lewis
Reg. No. 39,274
Attorney for Applicant(s)
Ryan, Mason & Lewis, LLP
90 Forest Avenue
Locust Valley, NY 11560
(516) 759-2946

Date: January 21, 2004

FORM PTO-1449 (MODIFIED)

Applicant(s): A. Satoh et al.
Docket No.: JP920020242US1
Serial No.: To Be Assigned
Filing Date: January 21, 2004
Group: To Be Assigned

**LIST OF PUBLICATIONS FOR
APPLICANT'S INFORMATION
DISCLOSURE STATEMENT**

U.S. PATENT DOCUMENTS

EXAMINER				FILING DATE
INITIAL	DOCUMENT NO.	DATE	NAME	IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

EXAMINER				TRANSLATION	
INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	YES NO

OTHER DOCUMENTS

EXAMINER		
INITIAL	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.

1. Johann Großschädl, "A Bit-Serial Unified Multiplier Architecture for Finite Fields GF (p) and GF (2^m)," CHES 2001, LNCS 2162, pp. 202-219, 2001.